EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	519	717/124.ccls.	USPAT	OR	OFF	2007/04/18 12:51
L2	319	717/128.ccls.	USPAT	OR	OFF	2007/04/18 12:51
L3	201	717/129.ccls.	USPAT	OR	OFF	2007/04/18 13:46
L4	12	("5321828" "5555783" "5935006" "6023727" "6067614" "6154857" "6167536" "6651243" "6658416" "6658557" "6658651" "6662313").pn.	USPAT	OR	OFF	2007/04/18 14:01
L5	17	(embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3)) and (switch or flag$4 or bit or "flip flop" or flipflop or enabl$4 or disabl$4 or (hardware near2 interrupt$3))	USPAT	OR	OFF	2007/04/18 16:01
L6	15	(embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3 or ice) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (length or size or number or distance or offset or relative) and (interrupt$3 or trap$4) and (current or next or following or previous or prior or successive) and (boot$3 or bios)	USPAT	OR	OFF	2007/04/18 14:35
L7	10	l6 not l5	USPAT	OR	OFF	2007/04/18 14:34
L8	39	(embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3)) and (switch or flag$4 or bit or "flip flop" or flipflop or enabl$4 or disabl$4 or (hardware near2 interrupt$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:36
L9	26	(embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3 or ice) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (length or size or number or distance or offset or relative) and (interrupt$3 or trap$4) and (current or next or following or previous or prior or successive) and (boot$3 or bios)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 14:36

EAST Search History

L10	53	l8 or l9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 15:56
L11	1012	(determin$3 or calculat$3) near3 ((length or size or distance) near3 instruction)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 15:58
L12	394	(determin$3 or calculat$3) near3 ((length or size or distance) near3 instruction) and (current and next) near3 instruction	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 15:59
L13	321	(determin$3 or calculat$3) near3 ((length or size or distance) near3 instruction) and (current and next) near3 instruction and start$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 15:59
L14	225	(determin$3 or calculat$3) near3 ((length or size or distance) near3 instruction) and (current and next) near3 instruction and start$3 and (interrupt$3 or trap$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:00
L15	1	(determin$3 or calculat$3) near2 ((length or size or distance) near2 instruction) and (current and next) near3 instruction and start$3 and (interrupt$3 or trap$4) and (embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3 or snoop$3 or prob$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:03

EAST Search History

L16	1	(determin$3 or calculat$3) near2 ((length or size or distance) near2 instruction) and (current and next) near3 instruction and start$3 and (interrupt$3 or trap$4) and (embed$4 or "on-board" or soc or "on-chip") near3 (debug$4 or trace or tracing or monitor$3 or snoop$3 or prob$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:02
L17	1	(determin$3 or calculat$3) near2 ((length or size or distance) near2 instruction) and (current and next) near3 instruction and start$3 and (interrupt$3 or trap$4) and (embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3 or snoop$3 or prob$3) same (rom or eprom or eeprom or dsp or (constrained near3 device) or"read only")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:04
L18	13	(determin$3 or calculat$3) near2 ((length or size or distance) near2 instruction) and (current and next) near3 instruction and start$3 and (interrupt$3 or trap$4) and (embed$4 or "on-board" or soc) and (debug$4 or trace or tracing or monitor$3 or snoop$3 or prob$3) same (rom or eprom or eeprom or dsp or (constrained near3 device) or"read only")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:04
L19	32	(determin$3 or calculat$3) near2 ((length or size or distance) near2 instruction) and (current and next) near3 instruction and start$3 and (interrupt$3 or trap$4) and (embed$4 or "on-board" or soc) and (debug$4 or trace or tracing or monitor$3 or snoop$3 or prob$3) and (rom or eprom or eeprom or dsp or (constrained near3 device) or"read only")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:04
L20	32	l18 or l19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:31

EAST Search History

L21	1930	714/25.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:35
L22	216	714/28.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:35
L23	957	714/30.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:35
L24	301	714/34.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:35
L25	275	714/35.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:35
L26	3	l21 and (embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3)) and (switch or flag$4 or bit or "flip flop" or flipflop or enabl$4 or disabl$4 or (hardware near2 interrupt$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:36

EAST Search History

L27	0	l22 and (embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3)) and (switch or flag$4 or bit or "flip flop" or flipflop or enabl$4 or disabl$4 or (hardware near2 interrupt$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:36
L28	5	l23 and (embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3)) and (switch or flag$4 or bit or "flip flop" or flipflop or enabl$4 or disabl$4 or (hardware near2 interrupt$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:36
L29	2	l24 and (embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3)) and (switch or flag$4 or bit or "flip flop" or flipflop or enabl$4 or disabl$4 or (hardware near2 interrupt$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:36
L30	1	l25 and (embed$4 or "on-board" or soc) near3 (debug$4 or trace or tracing or monitor$3) same (rom or eprom or eeprom or dsp or (constrained near3 device)) and (breakpoint$3 or (break$3 near2 point$3)) and (switch or flag$4 or bit or "flip flop" or flipflop or enabl$4 or disabl$4 or (hardware near2 interrupt$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/18 16:36

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Y Zorian
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E Kuzara

Embedded debug commands in a source file - group of 3 »
A Shridhar, J Simons... - US Patent 5,815,714, 1998 - Google Patents
... [54] **EMBEDDED DEBUG** COMMANDS IN A SOURCE FILE [75] Inventors: Avadhani Shridhar,
Sunnyvale; John Simons, Burlingame, both of Calif. ... **Embedded Debug** CMOS ...
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Reusable **embedded debugger** for 32 bit RISC processor using the JTAG boundary scan architecture - group of 3 »
DY Jung, SH Kwak, MK Lee - ASIC, 2002. Proceedings. 2002 IEEE Asia-Pacific Conference ..., 2002 - ieeexplore.ieee.org
Page 1 82-2-2123-4731 jdy2l ©spark.yonsei.ac.kr 82-2-2123-2867 mklee@mail.yonsei. ac.kr 209 Reusable **Embedded Debugger** for 32bit RISC Processor Using the JTAG ...
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Testing **embedded**-core-based system chips - group of 17 »
Y Zorian, EJ Marinissen, S Dey - Computer, 1999 - doi.ieeecomputersociety.org
... manufacturing test and **debug**. This paper ana- lyzes these challenges and discusses the current solutions to create testable and diagnosable **embedded** core-based ...
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System for analyzing and debugging **embedded** software through dynamic and interactive use of code ... - group of 3 »
EJ Kuzara, AJ Blasciak, GS Parets - US Patent 5,450,586, 1995 - Google Patents
... [54] SYSTEM FOR ANALYZING AND **DEBUGGING EMBEDDED** SOFTWARE THROUGH DYNAMIC AND
INTERACTIVE USE OF CODE MARKERS [75] Inventors: Eric J. Kuzara; Andrew J. Blasciak ...
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Emerging on-ship debugging techniques for real-time embeddedsystems - group of 5 »
C MacNamee, D Heffernan - Computing & Control Engineering Journal, 2000 - ieeexplore.ieee.org
... Global **Embedded Debug** Interlace, is introduccd and is rclaled lo Ihc test and **debugging** requireinelits of development engineers. ...
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Embedded control problems, Thumb, and the ARM7TDMI - group of 4 »
S Segars, K Clarke, L Goudge - Micro, IEEE, 1995 - ieeexplore.ieee.org
... The ideal processor for **embedded** control is thus a small. ... ARM7TDMI also con- tains **debugging** features (an EmbeddedICE macrocell ...
Cited by 66 - Related Articles - Web Search - BL Direct

A hardware-software co-simulator for **embedded** system design and debugging - group of 5 »
A Ghosh, M Bershteyn, R Casley, C Chien, A Jain, M ... - Proc. Asia South Pacific Design

Automation Conference, 1995 - sigda.org
... Abstract One of the interesting problems in hardware-software co-design is that of **debugging embedded** software in conjunction with hardware. ...
Cited by 19 - Related Articles - View as HTML - Web Search

Designing and debugging custom computing applications - group of 5 »
B Hutchings, B Nelson, MJ Wirthlin - Design & Test of Computers, IEEE, 2000 - ieeexplore.ieee.org
... would be much easier if the hardware **debugger** and sim- ulation **debugger** were merged into ... JHDL is a text-based design language that is **embedded** within the Java ...
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... Processors: A Perspective on Market Requirements, Processor Architectures and **Embedded** S/W Tools - group of 15 »
P Paulin, F Karim, P Bromley - Proceedings of the conference on Design, automation and test ..., 2001 - doi.ieeecomputersociety.org
... In conjunction with the **debugger**'s graphical user interface, support for special purpose datatypes used in **embedded** applications. ...
Cited by 46 - Related Articles - Web Search

A cost-effective RISC/DSP microprocessor for **embedded** systems - group of 4 »
M Dolle, M Schlett - Micro, IEEE, 1995 - ieeexplore.ieee.org
Page 1 32 IEEE Micro 0272-1732/951$04.OO© 1995 IEEE A Cost-Effective RISC/DSP Microprocessor for **Embedded** Systems Applications in telecommunications or ...
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1 Focus on Embedded Systems
Rick Lehrbaum
May 2001 **Linux Journal**
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(22.82 KB) Additional Information: full citation, abstract, index terms

The embedded side of LinuxWorld

2 Focus on embedded systems: Linux at the embedded systems conference
Rick Lehrbaum
July 2001 **Linux Journal**, Volume 2001 Issue 87
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(13.73 KB) Additional Information: full citation, index terms

3 Network processors: a perspective on market requirements, processor architectures and embedded S/W tools
P. Paulin, F. Karim, P. Bromley
March 2001 **Proceedings of the conference on Design, automation and test in Europe DATE '01**
Publisher: IEEE Press
Full text available: pdf(269.19 KB) Additional Information: full citation, references, citings, index terms

4 High-speed software-based platform for embedded software of a single-chip MPEG-2 video encoder LSI with HDTV scalability
Katsuyuki Ochiai, Hiroe Iwasaki, Jiro Naganuma, Makoto Endo, Takeshi Ogura
January 1999 **Proceedings of the conference on Design, automation and test in Europe DATE '99**
Publisher: ACM Press
Full text available: pdf(88.50 KB) Additional Information: full citation, citings, index terms

5 A selective, just-in-time aspect weaver
Yoshiki Sato, Shigeru Chiba, Michiaki Tatsubori
September 2003 **Proceedings of the 2nd international conference on Generative programming and component engineering GPCE '03**
Publisher: Springer-Verlag New York, Inc.
Full text available: pdf(256.62 KB) Additional Information: full citation, abstract, references, citings, index terms

Dynamic AOP (Aspect-Oriented Programming) is receiving growing interests in both the academia and the industry. Since it allows weaving aspects with a program at runtime, it is useful for rapid prototyping and adaptive software. However, the previous implementations of dynamic AOP systems suffered from serious performance penalties. This paper presents our new efficient dynamic AOP system in Java for addressing the underlying problem. This system called Wool is a hybrid of two approaches. When a ...

6 Design and specification of embedded systems in Java using successive, formal refinement
James Shin Young, Josh MacDonald, Michael Shilman, Abdallah Tabbara, Paul Hilfinger, A. Richard Newton
May 1998 **Proceedings of the 35th annual conference on Design automation DAC '98**
Publisher: ACM Press
Full text available: pdf(256.51 KB) Additional Information: full citation, abstract, references, citings, index terms

Successive, formal refinement is a new approach for specificationof embedded systems using a general-purpose programming language.Systems are formally modeled as Abstractable SynchronousReactive systems, and Java is used as the design inputlanguage. A policy of use is applied to Java, in the form of languageusage restrictions and class-library extensions, to ensureconsistency with the formal model. A process of incremental,user-guided program transformation is used to refine a Java programuntil ...

7 Am embedded system case study: the firm ware development environment for a multimedia audio processor
Clifford Liem, Marco Cornero, Miguel Santana, Pierre Paulin, Ahmed Jerraya, Jean-Marc Gentit, Jean Lopez, Xavier Figari, Laurent Bergher
June 1997 **Proceedings of the 34th annual conference on Design automation DAC '97**
Publisher: ACM Press
Full text available: pdf(52.50 KB) Additional Information: full citation, abstract, references, citings, index terms

This paper outlines a case study at SGS-Thomson Microelectronicson the development of a firmware development environment in co-operationwith Thomson Consumer Electronics Components. Theenviornment is for an embedded processor used for audiodecompression algorithms including: MPEG2, Dolby AC-3 Surround,and Dolby Pro-logic. The enabling component of the firmwareenvironment is a retargetable compiler which maps high-levelalgorithms onto the embedded processor. Although compilation is thecritical te ...

8 Standardization approach of ITRON debugging interface specification and evaluation of its adaptability
Takayuki Wakabayashi, Hiroaki Takada
June 2002 **ACM SIGPLAN Notices , Proceedings of the joint conference on Languages, compilers and tools for embedded systems: software and compilers for embedded systems LCTES/SCOPES '02**, Volume 37 Issue 7
Publisher: ACM Press
Full text available: pdf(180.57 KB) Additional Information: full citation, abstract, references, index terms

Debugging environments for embedded systems unavoidably depend on the internal

structure of the operating system (OS) in order to implement OS support functions. Since the ITRON specification standardizes only the API, the internal structure of operating systems conforming to the ITRON Specification are different, resulting in difficulties in supporting ITRON-Specification operating systems for debugging environments. To solve this problem, we design the ITRON Debugging Interface Specification w ...

Keywords: ITRON specification, OS-aware debugging environment, cross development system environment

9 Focus on embedded systems
Rick Lehrbaum
December 2001 **Linux Journal**, Volume 2001 Issue 92
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(20.23 KB) Additional Information: full citation, abstract, index terms

A Walk on the Embedded Side of LinuxWorld

10 Retargetable compiled simulation of embedded processors using a machine description language
Stefan Pees, Andreas Hoffmann, Heinrich Meyr
October 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 4
Publisher: ACM Press
Full text available: pdf(4.06 MB) Additional Information: full citation, abstract, references, citings, index terms

Fast processor simulators are needed for the software development of embedded processors, for HW/SW cosimulation systems, and for profiling and design of application-specific processors. Such fast simulators can be generated based on the machine description language LISA. Using this language to model processor architectures enables the generation of compiled simulators on various abstraction levels, assemblers, and compiler back ends. The article discusses the requirements of software devel ...

Keywords: DSP processors, HW/SW cosimulation, compiled simulation, instruction set simulators, machine description languages, processor modeling and simulation, system-on-chip

11 Embedding Linux in a Commercial Product: A look at embedded systems and what it takes to build one
Joel R. Williams
October 1999 **Linux Journal**
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(32.45 KB) Additional Information: full citation, index terms

12 LegoSim: simulation of embedded kernels over Pthreads
Thomas Röblitz, Frank Mueller, Oliver Bühn
March 2002 **Journal on Educational Resources in Computing (JERIC)**, Volume 2 Issue 1
Publisher: ACM Press
Full text available: pdf(273.72 KB) Additional Information: full citation, abstract, references, index terms

The contributions of this work are twofold. First, we describe the design and implementation of a simulation environment for an open-source embedded kernel and an

intuitive user interface to complement it. Second, the simulator can be used for embedded program development and research as well as instructional purposes in embedded system classes as a replacement or a complement to hands-on experiments with embedded devices.The technical sections of this article stress the suitability of POSIX Thr ...

Keywords: Computer architecture simulator, education, embedded systems, operating systems kernel simulator

13 Using Java reflection to automate extension language parsing

Dale Parson
December 1999 **ACM SIGPLAN Notices , Proceedings of the 2nd conference on Domain-specific languages PLAN '99**, Volume 35 Issue 1
Publisher: ACM Press
Full text available: pdf(1.03 MB) Additional Information: full citation, abstract, references, index terms

An extension language is an interpreted programming language designed to be embedded in a domain-specific framework. The addition of domain-specific primitive operations to an embedded extension language transforms that vanilla extension language into a domain-specific language. The LUxWORKS processor simulator and debugger from Lucent uses Tcl as its extension language. After an overview of extension language embedding and LUxWORKS experience, this paper looks at using Java reflection and ...

14 Embedded application design using a real-time OS

David Stepner, Nagarajan Rajan, David Hui
June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation DAC '99**
Publisher: ACM Press
Full text available: pdf(105.02 KB) Additional Information: full citation, references, citings, index terms

15 Embedded Systems News Briefs

Rick Lehrbaum
March 2000 **Linux Journal**
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(8.52 KB) Additional Information: full citation, abstract, index terms

%NULL

16 Tutorial 4: Embedded Software Development

November 2003 **Proceedings of the 2003 IEEE/ACM international conference on Computer-aided design ICCAD '03**
Publisher: IEEE Computer Society
Full text available: pdf(23.01 KB) Additional Information: full citation, index terms

17 Analysis of hardware and software approaches to embedded in-circuit emulation of microprocessors

Hsin-Ming Chen, Chung-Fu Kao, Ing-Jer Huang
January 2002 **Australian Computer Science Communications , Proceedings of the seventh Asia-Pacific conference on Computer systems architecture CRPIT '02**, Volume 24 Issue 3
Publisher: Australian Computer Society, Inc., IEEE Computer Society Press
Full text available: pdf(665.32 KB) Additional Information: full citation, abstract, references, index terms

This paper investigates various approaches to embed the functionality of in-circuit emulation (ICE) into microprocessor cores in SoC (System-On-Chip) chips. Three styles of ICE's (hardware-oriented, software-oriented and hybrid) are defined and implemented. They are integrated with a synthesizable ARM7 microprocessor core and synthesized to gate level to quantitatively analyze and compare their performance, cost and debugging features.

18 Regular contributions: More enhancements of the simplescalar tool set

Naraig Manjikian
September 2001 **ACM SIGARCH Computer Architecture News**, Volume 29 Issue 4
Publisher: ACM Press
Full text available: pdf(709.36 KB) Additional Information: full citation, abstract, references

An earlier paper described enhancements to the SimpleScalar tool set for functional multiprocessor simulation and visualization of cache coherence, and the software was made available at http://www.simplescalar.org. This paper describes additional enhancements to the SimpleScalar tool set. The enhancements include memory access visualization for uniprocessor and multiprocessor simulation, mnltiprocessor enhancement of the DLite! debugger that is included with SimpleScalar, modifications to the G ...

19 Software Streaming via Block Streaming

Pramote Kuacharoen, Vincent J. Mooney, Vijay K. Madisetti
March 2003 **Proceedings of the conference on Design, Automation and Test in Europe - Volume 1 DATE '03**
Publisher: IEEE Computer Society
Full text available: pdf(168.48 KB) Publisher Site Additional Information: full citation, abstract, index terms

Software streaming allows the execution of stream-enabled software on a device even while the transmission/streaming may still be in progress. Thus, the software can be executed while it is being streamed instead of causing the user to wait for the completion of download, decompression, installation and reconfiguration. Our streaming method can reduce application load time seen by the user since the application can start running as soon as the first executable unit is loaded into the memory. Fur ...

20 Experiences with building distributed debuggers

Michael S. Meier, Kevan L. Miller, Donald P. Pazel, Josyula R. Rao, James R. Russell
January 1996 **Proceedings of the SIGMETRICS symposium on Parallel and distributed tools SPDT '96**
Publisher: ACM Press
Full text available: pdf(1.34 MB) Additional Information: full citation, references, citings, index terms

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kernel ROM debug 1995 - 2004 Search

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All Results
B Mealey
J Dyer
J Held
R Swanberg
M Williams

Operating system debugger using **kernel** and dynamic extension with debugger drivers to support ... - group of 3 »
BG Mealey, RC Swanberg, MS Williams - US Patent 5,958,049, 1999 - Google Patents
... dev i ce or keyboard. This rebinding the **kernel** presents ... read-only memory (**ROM**).
After power to computer system the **debugger** . AS shown in FIG. ...
Cited by 14 - Related Articles - Web Search

System call issue method and **debug** system - group of 2 »
T Inui, T Muranaka, H Muraki, A Fukui - US Patent 5,964,890, 1999 - Google Patents
... 5 comprises an operating system **kernel** (an OS **kernel**) 51, a ... the user program is written
into a **ROM** after the completion of **debugging**, the **debug** task is no ...
Cited by 4 - Related Articles - Web Search

Apparatus and method for debugging/modifying **ROM**-based software systems employing and extensible ... - group of 2 »
PS Kasten - US Patent 5,740,351, 1998 - Google Patents
... Ki - $ illustrates a modification to a FORTH **kernel** employed in an exemplary ... ating
out of **ROM**. ... arises, a developer and/or **debugger** often needs to rely on ...
Cited by 1 - Related Articles - Web Search

A Debugger RTOS for Embedded Systems - group of 11 »
T Akgul, P Kuacharoen, VJ Mooney, VK Madisetti - 27th

Euromicro Conference, 2001 - doi.ieeecomputersociety.org
... code – ie, code inserted into the **kernel** to detect ... loadable **debugger** module: namely,
the **debugger** module can ... combines the advantages of **ROM** based **debugging** ...
Cited by 8 - Related Articles - Web Search

Real-Time Debugging of Digital Integrated Circuits
J Haufe, C Fritsch, M Gulbins, V Lück, P Schwarz - Proc. Design, Automation and Test in Europe Conference, User ..., 2000 - eas.iis.fhg.de
... Furthermore, the support of embedded DUT RAM and multiple- clock domain designs
are strong demands for system- on-chip **debugging**. ... Flash **ROM** OK ... HW **Debug Kernel** ...
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Kernel Korner: Porting Linux to the DEC. Alpha: The User Environment
J Paradis - Linux Journal, 1995 - portal.acm.org
... We discovered this when we were **debugging** the interrupt handling ... 3 will be based
on a 1.3 **kernel** and will ... on our PC64 system to use the **ROM debug** monitor, but ...
Web Search

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